A Specification-Based Approach to Reasoning about Pointers

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Motivation

Traditional static analysis techniques for pointers are limited in what they can prove about pointer programs.

```
01: typedef struct list {
     struct list *n;
02:
03: int data;
04: } List;
05:
06: List *splice(List *x, List *y) {
07: List *t = NULL;
08: List *z = y;
09: while (x != NULL) {
10: t = x;
11: x = t - n;
12: t - n = y - n;
13: y - > n = t;
14: y = y - > n - > n;
15:
     }
16:
     return z;
17: }
```



Example state at the beginning of splice

Example state at the end of splice

"Statically verify that, if the input lists x and y are disjoint and acyclic, then the list returned by splice is acyclic."

> Region-based shape analysis with tracked locations Hackett and Rugina, POPL 2005

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Shape analysis helps with some properties:

- Is a memory location referenced by more than one other location?
- Is a location accessed through a dangling references?
- Are memory leaks present?

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But it does not help with other properties:

 Does the splice operation do what it is supposed to do (i.e., does the operation interleave the elements of the incoming lists)?

Specification-Based Approach

- Provide a generic pointer component
- Include its full formal specification
- Give it a special implementation
- Special syntax is optional

Mathematical Model

Concept Location_Linking_Template (type Info);

Defines Location: Set; Defines Void: Location; Var Target: Location → Location; Var Contents: Location → Info; Var Is_Taken: Location → B;

Initialization ensures ∀q: Location, ¬Is Taken(q);

Type Position **is modeled by** Location; **exemplar** p; **Initialization ensures** p = Void;

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A System of Linked Locations



Operation Signatures

Concept Location_Linking_Template (type Info);
Type Position;
Operation Take_New_Location (updates p: Position);
Operation Abandon_Location (clears: p: Position);
Operation Relocate (updates p: Position; preserves q: Position);
Operation Follow_Link (updates p: Position);
Operation Redirect_Link (preserves p: Position; preserves q: Position);

end Location_Linking_Template;

...

Redirect Link

Operation Redirect_Link(**preserves** p: Position; **preserves** q: Position);

updates Target;
requires Is_Taken(p);
ensures ∀r: Location,

Target(r) = $\begin{cases} q & \text{if } r = p \\ #Target(r) & \text{otherwise} \end{cases};$



before

Redirect_Link(p, q)



before

Redirect_Link(p, q)



 $Target = \{ WAS \mapsto LIS, ... \}$ $Contents = \{ WAS \mapsto \alpha, \\ PAR \mapsto \beta, \\ LIS \mapsto \delta, ... \}$ $Is_Taken = \{ WAS \mapsto true, \\ PAR \mapsto true, \\ LIS \mapsto true, ... \}$ p = WAS q = PAR

Redirect_Link(p,q)



 $Target = \{ WAS \mapsto PAR, ... \}$ $Contents = \{ WAS \mapsto \alpha, \\ PAR \mapsto \beta, \\ LIS \mapsto \delta, ... \}$ $Is_Taken = \{ WAS \mapsto true, \\ PAR \mapsto true, \\ LIS \mapsto true, ... \}$ p = WAS q = PAR

Redirect_Link(p, q)



before

Relocate(p, q)



after

Relocate(p, q)



before









Implementation

Operation invocations such as • Relocate(p, q) • Follow_Link(p) • Redirect_Link(p, q) are implemented internally by copying a memory address, not by invoking an operation.

Splice Operation

Operation Splice (**preserves** p: Position; **clears** q: Position); **updates** Target;

- **precondition:** p and q point to disjoint and acyclic singly-linked lists of locations, and p's list is at least as long as q's
- **postcondition:** p's resulting list is an interleaving of q's incoming list with the first locations of p's incoming list.

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Is_Reachable_in (hops: N; p, q: Location): B
Is_Reachable (p, q: Location): B
Distance(p, q: Location): N



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Is_Reachable_in(3, p, q)

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Distance(p, q: Location): N



Is_Reachable_in(3, p, q)
Is_Reachable(p, Void)

Is_Reachable_in (hops: N; p, q: Location): B
Is_Reachable (p, q: Location): B
Distance(p, q: Location): N



Is_Reachable_in(3, p, q)
Is_Reachable(p, Void)
Distance(q, Void) = 1

Lightweight Specification

Operation Splice (**preserves** p: Position; **clears** q: Position); **updates** Target; **requires** ($\exists k_1, k_2 : N \ni$ Is_Reachable_in($k_1, p, Void$) and Is_Reachable_in($k_2, q, Void$) and $k_2 \le k_1$) and ($\forall r : Location$, **if** Is_Reachable(p, r) and Is_Reachable(q, r) **then** r = Void); **ensures** Is_Reachable(p, Void);

Splice Procedure

Operation Splice (preserves p: Position; clears q: Position); updates Target; Procedure

Var r: Position; Var s: Position; Relocate(r, p); While (not Is_At_Void(q)) do

Relocate(s, r); Follow_Link(r); Redirect_Link(s, q); Follow_Link(s); Follow_Link(q); Redirect_Link(s, r); end; end Splice;

While (not Is_At_Void(q)) do

Relocate(s, r);
Follow_Link(r);
Redirect_Link(s, q);
Follow_Link(s);
Follow_Link(q);
Redirect_Link(s, r);
end;



While (not Is_At_Void(q)) do

Relocate(s, r); Follow_Link(r); Redirect_Link(s, q); Follow_Link(s); Follow_Link(q); Redirect_Link(s, r); end;



While (not Is_At_Void(q)) do
















Procedure

Var r: Position; Var s: Position; Relocate(r, p); While (not Is_At_Void(q))

maintaining ???

do

Relocate(s, r); Follow_Link(r); Redirect_Link(s, q); Follow_Link(s); Follow_Link(q); Redirect_Link(s, r); end; end Splice;

Operation Splice (preserves p: Position; clears q: Position); updates Target; requires (∃k₁, k₂ : N∋ Is_Reachable_in(k₁, p, Void) and Is_Reachable_in(k₂, q, Void) and k₂ ≤ k₁) and (∀r: Location, if Is_Reachable(p, r) and Is_Reachable(q, r) then r = Void); ensures Is_Reachable(p, Void);

Procedure

Var r: Position; Var s: Position; Relocate(r, p); While (not Is_At_Void(q))

maintaining Is_Reachable(p, Void);

do

```
Operation Splice (preserves p: Position;
clears q: Position);
updates Target;
requires (∃k<sub>1</sub>, k<sub>2</sub> : N∋
Is_Reachable_in(k<sub>1</sub>, p, Void) and
Is_Reachable_in(k<sub>2</sub>, q, Void) and
k<sub>2</sub> ≤ k<sub>1</sub>) and
(∀r : Location,
if Is_Reachable(p, r) and Is_Reachable(q, r)
then r = Void);
ensures Is_Reachable(p, Void);
```

Procedure

Var r: Position; Var s: Position; Relocate(r, p); While (not Is_At_Void(q)) decreasing ??? maintaining Is_Reachable(p, Void);

do

Relocate(s, r); Follow_Link(r); Redirect_Link(s, q); Follow_Link(s); Follow_Link(q); Redirect_Link(s, r); end; end Splice;

Operation Splice (**preserves** p: Position; **clears** q: Position); **updates** Target; **requires** ($\exists k_1, k_2 : N \ni$ Is_Reachable_in(k_1 , p, Void) and Is_Reachable_in(k_2 , q, Void) and $k_2 \le k_1$) and ($\forall r : Location$, **if** Is_Reachable(p, r) and Is_Reachable(q, r) **then** r = Void); **ensures** Is_Reachable(p, Void);

Procedure

Var r: Position; Var s: Position; Relocate(r, p); While (not Is_At_Void(q)) decreasing Distance(q, Void); maintaining Is_Reachable(p, Void);

do

Relocate(s, r); Follow_Link(r); Redirect_Link(s, q); Follow_Link(s); Follow_Link(q); Redirect_Link(s, r); end; end Splice;

Operation Splice (preserves p: Position; clears q: Position); updates Target; requires (∃k₁, k₂ : N∋ Is_Reachable_in(k₁, p, Void) and Is_Reachable_in(k₂, q, Void) and k₂ ≤ k₁) and (∀r : Location, if Is_Reachable(p, r) and Is_Reachable(q, r) then r = Void); ensures Is_Reachable(p, Void);

Loop Invariant Proof

Procedure

Var r: Position; Var s: Position; Relocate(r, p); While (not Is_At_Void(q)) decreasing Distance(q, Void); maintaining Is_Reachable(p, Void); do Relocate(s, r);

Relocate(s, r); Follow_Link(r); Redirect_Link(s, q); Follow_Link(s); Follow_Link(q); Redirect_Link(s, r); end; end Splice;

- I. Initialization Is the invariant true at the start of the loop?
- 2. Maintenance Is the invariant true from one iteration to the next?
- 3. Termination –

Does the invariant allow you to prove what you need to?

Loop Invariant Proof

Lemma #1: Is_Reachable(q, Void); Lemma #2: Is_Reachable(r, Void); Lemma #3: Is_Reachable(p, r) **or** Is_Reachable(p, q);

Splice Operation

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String Notation

The following notations are defined in a module that specifies the properties of mathematical strings.

$\langle x \rangle$	a string containing x			
$\alpha \circ \beta$	the concatenation of strings α and β			
α ≤!≥ (β, γ)	asserts that α is a perfect shuffle of strings β and γ			

Definitions

Is_Info_Str(p, q: Location, α: Str(Info): **B**



Definitions

Is_Info_Str(p, q: Location, α: Str(Info): **B**



Is_Info_Str(p, q, $\langle \alpha, \beta, \gamma \rangle$)

Heavyweight Specification

Operation Splice (**preserves** p: Position; **clears** q: Position); updates Target; **requires** ** Same as in lightweight specification ** ensures (Vt: Location, if not Is_Reachable(#p, t) and **not** Is_Reachable(#q, t) **then** Target(t) = #Target(t)) **and** (\vee po, old_po, old_qo: Str(Info), if (Is_Info_Str(p, Void, po) and Is_Info_Str(#p, Void, old_po) and Is_Info_Str(#q, Void, old_qo)) then $po \le !\ge (old_po, old_qo));$



Let *rq_shuffle* be a perfect shuffle of info strings *ro* and *qo*.

Then $pr \circ rq_shuffle$ is a perfect shuffle of info strings old_po and old_qo .



Loop Invariant

maintaining (\forall t: Location, if not Is_Reachable(#p, t) and not Is_Reachable(#q, t) then Target(t) = #Target(t)) and (\forall pr, ro, qo, old_po, old_qo, rq_shuffle: Str(Info), if (Is_Info_Str(p, r, po) and Is_Info_Str(r, Void, ro) and Is_Info_Str(q, Void, qo) Is_Info_Str(#p, Void, old_po) and Is_Info_Str(#q, Void, old_qo) and rq_shuffle $\leq !\geq$ (ro, qo) then pr \circ rq_shuffle $\leq !\geq$ (old_po, old_qo));

Loop Invariant

maintaining (∀t: Location, if not Is_Reachable(#p, t) and not Is_Reachable(#q, t) then Target(t) = #Target(t)) and (∀pr, ro, qo, old_po, old_qo, rq_shuffle: Str(Info), if (Is_Info_Str(p, r, po) and Is_Info_Str(r, Void, ro) and Is_Info_Str(q, Void, qo) Is_Info_Str(#p, Void, old_po) and Is_Info_Str(#q, Void, old_qo) and rq_shuffle ≤!≥ (ro, qo) then pr o rq_shuffle ≤!≥ (old_po, old_qo));

Sketch of Proof

Initialization

We need to show $pr \circ rq_shuffle \le! \ge (old_po, old_qo)$ Since p = r, it suffices to show empty_string $\circ pq_shuffle \le! \ge (old_po, old_qo)$

Termination

We know $pr \circ rq_shuffle \le!\ge (old_po, old_qo)$ Since $q = Void, rq_shuffle = ro, we know$ $pr \circ ro \le!\ge (old_po, old_qo)$

Sketch of Proof

Maintenance

Assume: $pr \circ rq_shuffle \le! \ge (old_po, old_qo)$ Show: $pr' \circ rq_shuffle' \le! \ge (old_po, old_qo)$

 $ro = \langle x \rangle \circ ro'$ $qo = \langle y \rangle \circ qo'$ $rq_shuffle = \langle x \rangle \circ \langle y \rangle \circ rq_shuffle'$ $ps' = pr \circ \langle x \rangle$ $pr' = pr \circ \langle x \rangle \circ \langle y \rangle$ $pr' \circ rq_shuffle' = pr \circ rq_shuffle$

Summary

Traditional Analysis of Pointers

- Fully automated
- Relatively fast
- Low (but present) false positive rate
- Limited in what it can prove

Specification-Based Approach

- Partly automated
- Requires programmer supplied assertions
- Handles both lightweight and heavyweight specifications

Related Work

- Region-based shape analysis
- Logic of stores
- Constraint solver with Alloy
- ESC/Java
- LOOP compiler
- Safe pointers and checked pointers
- Pointer component



Additional Slides

Objective

Allow programmers to reason about pointers and programs that involve pointers using the same techniques they use to reason about programs without pointers.

- The technique should not require special language semantics or proof rules that apply only to pointers.
- The technique should not be limited in what it can prove about programs with pointers.

Approach

Introduce a fully specified component into the programming language that captures the functional and performance behavior of pointers.

- Programmers can reason about pointers as they reason about any other component.
- For appropriate performance behavior, the compiler must implement the component differently than it implements other components.

Shape Analysis Memory Abstraction



"Each configuration characterizes the state of one single heap location, called the *tracked location*."



before



Swap_Contents(p, t)

Shape Analysis Overview

Reason about invariants that describe the "shapes" of dynamic data structures.

- A memory location is not referenced by more than one other location.
- A tree structure is maintained by a program.
- A list does not contain cycles.
- No accesses through dangling references.
- Memory leaks do not occur.

Shape Analysis: Tracking the State

	Y1	X1	L1			
08: List *z = y;		Ţ	Ţ			
	Y1 Z1	X1	AUGINESS E LI			
09: while (x != NULL)			-			
	Y1 Z1 Z1	X1	T1 L1 L1	Y1 L1	Y1 T1 L1	
10: $t = x;$						
	Y1 Z1 Z1	X1 T1	lin sin <mark>L1</mark>	Y1 L1		
11: $x = t - > n;$				-		
	Y1 Z1 Z1	T1	L1	Y1 L1	× X1 L1	X1 Y1 L1
12: $t - > n = v - > n$;	in action					
	Y1 Z1 Z1	T1	L2 L1	Y1 L1 Y1 L2	X1 L1 X1	X1 Y1 L1
12			tn yn ta yn	th yn th yn	tn yn	tn yn
13: $y - 2n - t_i$	Y1 Z1 Z	TILI	LI	Y1 L1	XI	X1 Y1
		yn	yn	yn		
14: $y = y - > n - > n;$	71			VIII	×1	
16: return z;						
	21		.1			

Shape Analysis Input state and abstraction



Shape Analysis Output state and abstraction



Shape Analysis L2 presents a problem



Shape Analysis L2 with acyclic output


Pointer Component



before

Redirect_Link(p, q)



after

Redirect_Link(p, q)

Splice Operation

Definitions

Definition Var Is_Reachable_in (hops: **N**; p, q: Location): **B** = Target^{hops}(p) = q **and** \forall k: **N**, **if** Target^k(p) = q **then** k ≥ hops;



Is_Reachable_in(3, p, q)

Definitions

Definition Var Is_Reachable (p, q: Location): **B** = \exists k: N \ni Is_Reachable_in(k, p, q);



Is_Reachable(p, Void)

Definitions

Definition Var Distance(p, q: Location): N = $\begin{cases} k & \text{if Is}_Reachable_in(k, p, q) \\ 0 & \text{otherwise} \end{cases}$;



Distance(q, Void) = 1

Definition

Definition Var Is_Info_Str(p, q: Location, α : Str(Info): **B** = **I** n: **N** \ni Is_Reachable_in(n, p, q) **and** $\alpha = \prod_{k=1..n} \langle \text{Contents}(\text{Target}^k(p)) \rangle;$

$$\begin{array}{c} p & q \\ \hline \alpha & \beta & \gamma & \delta \\ \hline \end{array}$$

Is_Info_Str(p, q, $\langle \alpha, \beta, \gamma \rangle$)