

AES Key Schedule

$8 \times 16 = 128$ bits
 32 bits (4 bytes) $w[0] \dots w[3]$
 $w[4] \dots w[7]$
 R0 key
 R1 key
 $w[44] \dots w[47]$

Key Exp (byte key[16], word $w[44]$)

Round 0
key is
the
original
key

for word tmp:
 for (int i = 0; i < 4; i++)
 $w[i] = (\text{key}[4i], \text{key}[4i+1], \text{key}[4i+2], \text{key}[4i+3])$
 concat the next 4 bytes

for (int i = 4; i < 44; i++) {

tmp = w[i-1]

Usually skipped { if (i % 4 == 0) // Once a round
 $\text{tmp} = \text{Subword}(\text{Rotword}(\text{tmp})) \oplus (\text{Rcon}[i/4], 0, 0, 0)$
 S box Left cyclic rotation by 1 byte

$w[i] = w[i-4] \oplus \text{tmp}$

w[23] = ab 73 29 cd

w[20] = 65 90 bb a4

Goal w[24]

→ w[5] box

temp	RotWord	SubWord	Recon	After XOR
ab 73 29 cd	<u>73</u> <u>29</u> <u>cd</u> <u>ab</u>	<u>8f</u> <u>a5</u> <u>bd</u> <u>62</u>	20 00 00 00	af a5 bd 62

AF AS BD 62

⊕ 65 90 BB A4

CA 35 06 C6

2nd example

~~0110
1110~~

w[i-4] w[31] = 01 23 45 67

w[i-1] w[34] = 89 ab cd ef ⊕

w[35] = 88 88 88 88