

UCF

**School of Computer Science
CDA 4150 Computer Architecture
Summer 2005**

Project

Due Monday 7/25/05

In this research project you have to study and submit a report of the IA-32 architectures; the report must include the following:

- Description of how the memory addressing mechanism works.
- Description of the protection ring mechanism.
- Interrupt handling mechanism.
- Bus speed
- Cache – speed, size and type
- Clock speed
- Registers: how many and the function of each one. For instance, control registers, descriptor registers, address registers, data registers, and so on.
- Instruction and descriptor formats.